

Amendments to the Claims:

1. (currently amended) A storage virtualization computer system comprising:

a host entity for issuing IO requests;

an external storage virtualization controller coupled to said host entity for executing

5 IO operations in response to said IO requests; and

at least one physical storage device (PSD), each coupled to the storage virtualization controller through a point-to-point serial-signal interconnect, for providing storage to the storage virtualization computer system through the storage virtualization controller;

10

wherein said storage virtualization controller comprises:

a central processing circuitry for performing IO operations in response to said IO requests of said host entity;

15 at least one IO device interconnect controller coupled to said central processing circuitry;

at least one host-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to said host entity; and

20 at least one device-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to a said at least one physical storage device.

2. (original) The storage virtualization computer system of claim 1 wherein said point-to-point serial-signal interconnect is a Serial ATA IO device interconnect.

25

3. (currently amended) The computer system of one of claims 1 and 2, wherein [[a]] said at least one PSD comprises a SATA PSD.

4. (currently amended) The computer system of one of claims 1 and 2, wherein [[a]] said at least one PSD comprises a PATA PSD and a serial-to-parallel converter is provided between a said device-side IO device interconnect ~~controller~~ port and said PATA PSD.

5

5. (original) The computer system of one of claims 1 and 2 further comprising a detachable canister attached to said storage virtualization controller for containing a said at least one PSD therein.

10 6. (currently amended) The computer system of one of claims 1 and 2, wherein [[a]] said at least one PSD can be detached from said storage virtualization controller when said storage virtualization controller is on-line.

15 7. (currently amended) The computer system of one of claims 1 and 2, wherein [[a]] said at least one PSD can be attached to said storage virtualization controller when said storage virtualization controller is on-line.

20 8. (original) The computer system of one of claims 1 and 2, wherein said storage virtualization controller is configured to define at least one logical media unit consisting of sections of at least one said PSD.

9. (cancelled)

25 10. (currently amended) The storage virtualization computer system of claim 1 ~~claim 9~~ wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port are provided in a ~~the~~ same IO device interconnect controller.

11. (currently amended) The storage virtualization computer system of claim 1

~~claim 9~~ wherein said at least one IO device interconnect controller comprises a plurality of IO device interconnect controllers; wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port are provided in different IO device interconnect controllers.

5

12. (original) The computer system of one of claims 1 and 2, wherein said storage virtualization controller comprises a plurality of host-side IO device interconnect ports each for coupling to a host-side IO device interconnect.

10 13. (original) The computer system of claim 12, wherein said storage virtualization controller is configured to present redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.

14. (currently amended) The computer system of claim 1 ~~claim 9~~, wherein at
15 least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.

15. (currently amended) The computer system of claim 1 ~~claim 9~~, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop
20 connectivity in target mode.

16. (currently amended) The computer system of claim 1 ~~claim 9~~, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop connectivity in target mode.

25

17. (currently amended) The computer system of claim 1 ~~claim 9~~, wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

18. (currently amended) The computer system of claim 1 ~~claim 9~~, wherein at least one said host-side IO device interconnect port is ethernet supporting the iSCSI protocol operating in target mode.

5

19. (currently amended) The computer system of claim 1 ~~claim 9~~, wherein at least one said host-side IO device interconnect port is Serial-Attached SCSI (SAS) operating in target mode.

10 20. (currently amended) The computer system of claim 1 ~~claim 9~~, wherein at least one said host-side IO device interconnect port is Serial ATA operating in target mode.

15 21. (currently amended) A storage virtualization subsystem for providing storage to a host entity, comprising:
an external storage virtualization controller for connecting to the host entity and executing IO operations in response to IO requests issued from said host entity;
and
at least one physical storage device, each coupled to the storage virtualization controller through a point-to-point serial-signal interconnect, for providing
20 storage to the host entity through the storage virtualization controller;

wherein said storage virtualization controller comprises:

25 a central processing circuitry for performing IO operations in response to said IO requests of said host entity;
at least one IO device interconnect controller coupled to said central processing circuitry;

at least one host-side IO device interconnect port provided in a said at least one IO
device interconnect controller for coupling to said host entity; and
at least one device-side IO device interconnect port provided in a said at least one
IO device interconnect controller for coupling to a said at least one physical
5 storage device.

22. (original) The storage virtualization subsystem of claim 21 wherein said
point-to-point serial-signal interconnect is a Serial ATA IO device interconnect.

10 23. (cancelled)

24. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23 wherein a said host-side IO device interconnect port and a said device-side IO device
interconnect port are provided in a the same IO device interconnect controller.

15 25. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23 wherein said at least one IO device interconnect controller comprises a plurality of IO
device interconnect controllers; wherein a said host-side IO device interconnect port and a
said device-side IO device interconnect port are provided in different IO device
20 interconnect controllers.

26. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein ~~[[a]]~~ said at least one PSD comprises a SATA PSD.

25 27. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein said storage virtualization controller comprises a plurality of host-side IO
device interconnect ports each for coupling to a host-side IO device interconnect.

28. (currently amended) The storage virtualization subsystem of claim 21 ~~claim 23~~, wherein said storage virtualization controller is configured to define at least one logical media unit consisting of sections of said at least one PSD.

5 29. (original) The storage virtualization subsystem of claim 27, wherein said storage virtualization controller is configured to present redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.

10 30. (currently amended) The storage virtualization subsystem of claim 21 ~~claim 23~~, wherein said at least one PSD comprises a PATA PSD and a serial-to-parallel converter is provided between a said device-side IO device interconnect ~~controller~~ port and said PATA PSD.

15 31. (currently amended) The storage virtualization subsystem of claim 21 ~~claim 23~~ further comprising a detachable canister attached to said storage virtualization controller for containing a said PSD therein.

20 32. (currently amended) The storage virtualization subsystem of claim 21 ~~claim 23~~, wherein ~~[[a]]~~ said at least one PSD can be detached from said storage virtualization controller when said storage virtualization controller is on-line.

25 33. (currently amended) The storage virtualization subsystem of claim 21 ~~claim 23~~, wherein ~~[[a]]~~ said at least one PSD can be attached to said storage virtualization controller when said storage virtualization controller is on-line.

34. (currently amended) The storage virtualization subsystem of claim 21 ~~claim 23~~, wherein said storage virtualization controller further comprises at least one multiple-device device-side expansion port for accommodating an additional set of at

least one PSD.

35. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein at least one said host-side IO device interconnect port is Fibre Channel
5 supporting point-to-point connectivity in target mode.

36. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein at least one said host-side IO device interconnect port is Fibre Channel
supporting private loop connectivity in target mode.

10

37. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein at least one said host-side IO device interconnect port is Fibre Channel
supporting public loop connectivity in target mode.

15 38. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein at least one said host-side IO device interconnect port is parallel SCSI
operating in target mode.

39. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
20 23, wherein at least one said host-side IO device interconnect port is ethernet supporting
the iSCSI protocol operating in target mode.

40. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein at least one said host-side IO device interconnect port is Serial-Attached
25 SCSI (SAS) operating in target mode.

41. (currently amended) The storage virtualization subsystem of claim 21 ~~claim~~
23, wherein at least one said host-side IO device interconnect port is Serial ATA

operating in target mode.

42. (currently amended) The storage virtualization subsystem of claim 21 ~~claim 23~~ further comprising an enclosure management services mechanism.

5

43. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism manages and monitors at least one of the following devices belonging to the storage virtualization subsystem: power supplies, fans, temperature sensors, voltages, uninterruptible power supplies, batteries, LEDs, audible
10 alarms, PSD canister locks, door locks.

44. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration.

15

45. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support device-forwarded EMS configuration.

20 46. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration and device-forwarded EMS configuration.

25 47. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support SES enclosure management services protocol.

48. (original) The storage virtualization subsystem of claim 42, wherein said

enclosure management services mechanism is configured to support SAF-TE enclosure management services protocol.

49. (original) The storage virtualization subsystem of claim 42, wherein said
5 EMS mechanism further comprises I2C latches to communicate with said storage virtualization controller.

50. (original) The storage virtualization subsystem of claim 42, wherein said
EMS mechanism further comprises status-monitoring circuitry to communicate with said
10 storage virtualization controller.

51. (original) The storage virtualization subsystem of claim 42, wherein said
EMS mechanism further comprises the following mechanism to communicate with said
storage virtualization controller: I2C latches and status-monitoring circuitry.
15

52. (original) The storage virtualization subsystem of claim 42, wherein said
EMS mechanism further comprises a CPU for running a program.

53. (original) The storage virtualization subsystem of claim 42, wherein said
20 EMS mechanism further comprises at least one I2C interconnect as a primary communication media to said storage virtualization controller.

54. (withdrawn) An external storage virtualization controller for executing IO
operations in response to IO requests from a host entity, comprising:
25 a central processing circuitry for performing IO operations in response to said IO requests of said host entity;
at least one IO device interconnect controller coupled to said central processing circuitry;

at least one host-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to said host entity; and
at least one device-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to and performing
5 point-to-point serial-signal transmission with at least one physical storage device.

55. (withdrawn) The external storage virtualization controller of claim 54 wherein said device-side IO device interconnect controller comprises at least one Serial
10 ATA port, each for connecting to a said at least one physical storage device through a Serial ATA IO device interconnect.

56. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein a said host-side IO device interconnect port and a said device-side IO
15 device interconnect port are provided in the same IO device interconnect controller.

57. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port are provided in different IO device interconnect controllers.

20 58. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein said device-side IO device interconnect controller further comprises a PCI/PCI-X interface for connecting to the central processing circuit.

25 59. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein said device-side IO device interconnect controller further comprises a PCI Express interface for connecting to the central processing circuit.

60. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein said storage virtualization controller comprises a plurality of host-side IO device interconnect ports each for coupling to a host-side IO device interconnect.

5

61. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein said storage virtualization controller is configured for defining at least one logical media unit consisting of sections of said at least one PSD.

10

62. (withdrawn) The external storage virtualization controller of claim 60, wherein said storage virtualization controller is configured for presenting redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.

15

63. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said PSD is a direct-access storage device (DASD) and said storage virtualization controller is configured for defining a logical media unit consisting of sections of at least one said direct access storage device and said logical media unit is of RAID level or a combination of RAID levels, whereby said logical media unit is contiguously addressable by said host entity.

20

64. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 further comprising at least one multiple-device device-side expansion port for accommodating a second set of at least one PSD.

25

65. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.

66. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop connectivity in target mode.

5 67. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop connectivity in target mode.

10 68. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

15 69. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is ethernet supporting the iSCSI protocol operating in target mode.

20 70. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Serial-Attached SCSI (SAS) operating in target mode.

 71. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Serial ATA operating in target mode.

25 72. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 further comprising an enclosure management services mechanism.

 73. (withdrawn) The storage virtualization controller of claim 72, wherein said

enclosure management services mechanism is configured to support direct-connect EMS configuration.

74. (withdrawn) The storage virtualization controller of claim 72, wherein said
5 enclosure management services mechanism is configured to support device-forwarded EMS configuration.

75. (withdrawn) The storage virtualization controller of claim 72, wherein said
enclosure management services mechanism is configured to support direct-connect EMS
10 configuration and device-forwarded EMS configuration.

76. (withdrawn) The storage virtualization controller of claim 72, wherein said
storage virtualization controller is configured to support SES enclosure management
services protocol.

15

77. (withdrawn) The storage virtualization controller of claim 72, wherein said
storage virtualization controller is configured to support SAF-TE enclosure management
services protocol.

20 78. (currently amended) A method for performing storage virtualization in a
computer system with an external storage virtualization controller of the computer system,
the method comprising:

receiving an IO request from a host entity of the computer system with the storage
virtualization controller;
25 parsing the IO request with the storage virtualization controller to decide at least one
IO operation to perform in response to said IO request; ~~and~~
performing at least one IO operation with the storage virtualization controller to
access at least one physical storage device of the computer system in

point-to-point serial-signal transmission; and
performing said at least one IO operation by a central processing circuitry in the
storage virtualization controller in response to said IO request of said host
entity;

5 wherein said storage virtualization controller includes:

at least one IO device interconnect controller coupled to said central processing
circuitry;

10 at least one host-side IO device interconnect port provided in a said at least one IO
device interconnect controller for coupling to said host entity; and

at least one device-side IO device interconnect port provided in a said at least one
IO device interconnect controller for coupling to a said at least one physical
storage device.

15 79. (original) The method of claim 78 wherein the point-to-point serial-signal
transmission is performed in a format complying with a Serial ATA protocol.

80. (original) The method of one of claims 78 and 79 further comprising the step of
providing an enclosure management services mechanism.

20

81. (original) The method of claim 80 further comprising the step of executing said
enclosure management services mechanism when said mechanism is configured to
support direct-connect EMS configuration.

25 82. (original) The method of claim 80 further comprising the step of executing said
enclosure management services mechanism when said mechanism is configured to
support device-forwarded EMS configuration.

83. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said mechanism is configured to support direct-connect EMS configuration and device-forwarded EMS configuration.

5 84. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said storage virtualization controller is configured to support SES enclosure management services protocol.

10 85. (original) The method of claim 80, further comprising the step of executing said enclosure management services mechanism when said storage virtualization controller is configured to support SAF-TE enclosure management services protocol.

15 86. (currently amended) The method of one of claims 78 and 79, wherein said at least one ~~said~~ PSD comprises a SATA PSD.

 87. (currently amended) The method of one of claims 78 and 79, wherein said at least one ~~said~~ PSD comprises a PATA PSD and serial signals in said serial signal transmission are converted by a serial-to-parallel converter to parallel signals compliant with said PATA PSD.

20 88. (original) The method of one of claims 78 and 79, wherein the step of performing said at least one IO operation comprises issuing at least one device-side IO request to said device-side IO device interconnect controller and re-formatting said device-side IO request and accompanying IO data into at least one data packet for
25 transmission.

 89. (original) The method of claim 88, wherein said data packet comprises a start segment at the beginning indicating the start of said data packet, an end segment at the

end indicating the end of the data packet, a payload data segment containing actual IO information to transmit through the device-side IO device interconnect, and a check data segment containing check codes derived from said payload data for checking the correctness of said payload data after transmission.

5

90. (currently amended) A computer-readable storage medium having a computer program code stored therein that is capable of causing a computer system having an external storage virtualization controller and at least one physical storage device connected to the storage virtualization controller to perform the steps of:

- 10 receiving an IO request from a host entity of the computer system with the storage virtualization controller;
- parsing the IO request with the storage virtualization controller to decide at least one IO operation to perform in response to said IO request; ~~and~~
- performing said at least one IO operation with the storage virtualization controller to
- 15 access said at least one physical storage device in point-to-point serial-signal transmission; and
- performing said at least one IO operation by a central processing circuitry in the storage virtualization controller in response to said IO request of said host entity;

20

wherein said storage virtualization controller includes:

- at least one IO device interconnect controller coupled to said central processing circuitry;
- 25 at least one host-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to said host entity; and
- at least one device-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to a said at least one physical

storage device

91. (original) The computer-readable storage medium of claim 90 wherein the point-to-point serial-signal transmission is performed in a format complying with a Serial
5 ATA protocol.

92. (currently amended) The computer-readable storage medium of one of claims 90 and 91, wherein [[a]] said at least one PSD comprises a SATA PSD.

10 93. (currently amended) The computer-readable storage medium of one of claims 90 and 91, wherein [[a]] said at least one PSD comprises a PATA PSD and serial signals in said serial signal transmission are converted by a serial-to-parallel converter to parallel signals compliant with said PATA PSD.

15 94. (original) The computer-readable storage medium of one of claims 90 and 91, wherein the step of performing said at least one IO operation comprises issuing a device-side IO request to said device-side IO device interconnect controller and re-formatting said device-side IO request and accompanying IO data into at least one data packet for transmission.

20 95. (original) The computer-readable storage medium of claim 94, wherein said data packet comprises a start segment at the beginning indicating the start of said data packet, an end segment at the end indicating the end of the data packet, a payload data segment containing actual IO information to transmit through the device-side IO device
25 interconnect, and a check data segment containing check codes derived from said payload data for checking the correctness of said payload data after transmission.

96. (new) The storage virtualization subsystem of claim 21, wherein said storage

Appl. No. 10/707,871
Amdt. dated April 27, 2007
Reply to Office action of December 27, 2006

virtualization controller further comprises a plurality of redundantly-configured multiple-device device-side expansion ports for redundantly accommodating an additional set of PSDs.

5